# Display Elektronik GmbH

# DATA SHEET

LCD-MODULE

# **DEM 16231 FGH-PW**

**Product Specification** 

Ver.: 1

# **Revision History**

VERSION	DATE	Note
0	19.03.2015	First Issue
1	25.02.2016	Modify Precautions in use of LCD Modules & Static Electricity Test

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# 1. General Specification

■ Number of Characters: 16 Characters x 2 Lines

■ Module Dimension: 85.00 x 30.00 x 6.20 mm

■ View Area: 76.00 x 18.00 mm

Active Area: 73.81 x 15.88 mm

■ Dot Size: 0.75 x 0.90 mm

■ Dot Pitch: 0.79 x 0.97 mm

■ Character Size: 3.91 x 7.69 mm

■ Character Pitch: 4.66 x 8.19 mm

LCD type: FSTN Positive Transflective,

■ Duty: 1/16 , 1/5 Bias

View Direction: 6 o'clock

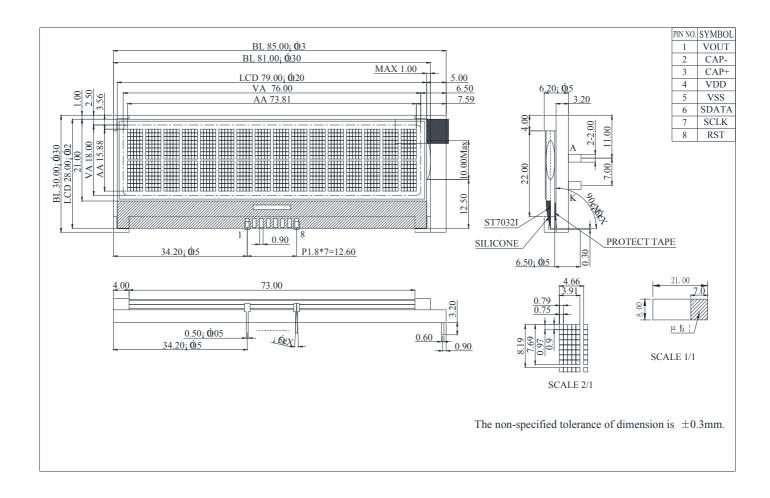
■ Backlight Type: LED, White

■ IC: ST7032i (Sitronix)

# 2. Interface Pin Function

Pin No.	Symbol	Level	Description		
1	VOUT		DC/DC voltage converter. Connect a capacitor between this terminal and VIN when the built-in booster is used.		
2	CAP-		For voltage booster circuit (VDD-VSS)		
3	CAP+		External capacitor about 0.1uF~4.7uF		
4	VDD	3.0/5.0V	Power supply		
5	VSS		GND		
6	SDATA		(In I2C interface DB7 (SDATA) is input data. SDATA and SCLK must connect to I2C bus (I2C bus is to connect a resister between SDATA/ SCLK and the power of I2C bus).		
7	SCLK		(In I2C interface DB6 (SCLK) is clock input. SDA and SCLK must connect to I2C bus (I2C bus is to connect a resister between SDATA/SCLK and the power of I2C bus).		
8	RST		RESET (Low active)		

# 3. Counter Drawing



# **Application schematic**

# VDD=3.0V

1	VOUT	
2	CAP-	
3	CAP+	TIUF TIUF
4	VDD	VDD TIEF
5	VSS	VSS_
6	SDATA	\$ 1000
7	SCLK	₹ 10K VDD
8	RST	

# VDD=5.0V

1	VOUT	NG.
2	- CAP-	NC NC
3	CAP+	<u> </u>
4	VDD	VDD
5	VSS	VSS
6	SDATA	S tory
7	SCLK	\$10K VDD
8	RST	

INITIALIZE: (3V)

MOV I2C\_CONTROL,#00H; WRITE COMMAND

MOV I2C\_DATA,#38H ;Function Set

LCALL WRITE\_CODE

MOV I2C\_CONTROL,#00H ;WRITE COMMAND

MOV I2C\_DATA,#39H ;Function Set

LCALL WRITE CODE

MOV I2C DATA,#14H ;Internal OSC frequency

LCALL WRITE\_CODE

MOV I2C\_DATA,#74H ;Contrast set

LCALL WRITE\_CODE

MOV I2C DATA,#54H ;Power/ICON control/Contrast set

LCALL WRITE CODE

MOV I2C DATA,#6FH ;Follower control

LCALL WRITE\_CODE

MOV I2C\_DATA,#0CH ;Display ON/OFF

LCALL WRITE\_CODE

MOV I2C\_DATA,#01H ;Clear Display

LCALL WRITE\_CODE

**INITIALIZE: (5V)** 

MOV I2C CONTROL,#00H;WRITE COMMAND

MOV I2C DATA,#38H ;Function Set

LCALL WRITE CODE

MOV I2C\_CONTROL,#00H;WRITE COMMAND

MOV I2C\_DATA,#39H ;Function Set

LCALL WRITE CODE

MOV I2C\_DATA,#14H ;Internal OSC frequency

LCALL WRITE\_CODE

MOV I2C DATA,#79H ;Contrast set

LCALL WRITE\_CODE

MOV I2C\_DATA,#50H ;Power/ICON control/Contrast set

LCALL WRITE\_CODE

MOV I2C DATA,#6CH ;Follower control

LCALL WRITE\_CODE

MOV I2C\_DATA,#0CH ;Display ON/OFF

LCALL WRITE CODE

MOV I2C DATA,#01H ;Clear Display

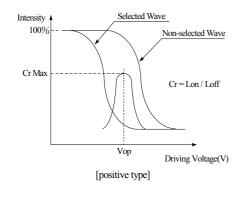
LCALL WRITE\_CODE

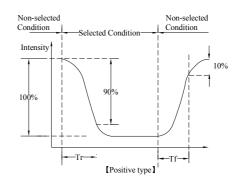
# 4. Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
	θ	CR <b>≧</b> 2	0	_	30	ψ= 180°
View Angle	θ	CR <b>≧</b> 2	0	_	60	ψ= 0°
	θ	CR <b>≧</b> 2	0	_	45	ψ= 90°
	θ	CR <b>≧</b> 2	0	_	45	ψ= 270°
Contrast Ratio	CR	_	_	5	_	_
Doononee Time	T Rise	_	_	150	200	ms
Response Time	T Fall	_	_	150	200	ms

### **Definition of Operation Voltage (Vop)**

#### Definition of Response Time (Tr, Tf)

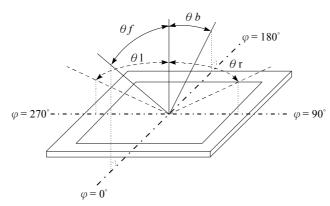




#### **Conditions:**

Frame Frequency: 64 Hz Driving Waveform: 1/N Duty, 1/a Bias

#### Definition of viewing angle (CR≥2)



# 5. Absolute Maximum Ratings

Item	Symbol	Min	Тур	Max	Unit
Operating Temperature	Тор	-20	_	+70	°C
Storage Temperature	Тѕт	-30	_	+80	°C
Input Voltage	VIN	-0.3	_	V <sub>DD</sub> +0.3	V
Power Supply Voltage	V <sub>DD</sub> -V <sub>SS</sub>	-0.3	_	+6.0	V
LCD Driver Voltage	V <sub>LCD</sub>	2.7	_	7.0	V

## 6. Electrical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
					5	
Supply Voltage For Logic	$V_{\text{DD}}$ - $V_{\text{SS}}$	_	3	3.3	(bon=1	V
					max=3.5V)	
		Ta=-20°C	_	_	_	V
Supply Voltage For LCD	$V_{LCD}$	Ta=25°C	4.3	4.5	4.7	V
		Ta=70°C	_	_	_	V
Input High Volt.	V <sub>IH</sub>	_	0.7 V <sub>DD</sub>	_	$V_{DD}$	V
Input Low Volt.	VIL	_	_	_	0.2 V <sub>DD</sub>	V
Output High Volt.	Vон	_	0.8 V <sub>DD</sub>	_	$V_{DD}$	V
Output Low Volt.	V <sub>OL</sub>	_	_	_	0.2V <sub>DD</sub>	V
Supply Current(No	laa				0.0	mA
include LED Backlight)	ldd	_	_	_	2.0	IIIA

Please kindly consider to design the Vop to be adjustable while programing the software to match LCD contrast tolerance.

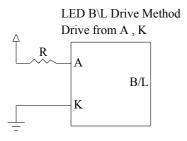
# 7. Backlight Information

#### **Specification**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	_	32	40	mA	V=3.5V
Supply Voltage	V	3.4	3.5	3.6	V	
Reverse Voltage	VR	_	_	5	V	_
Color coordinate	Х	0.24	0.26	0.28		V=3.5V
Color coordinate	Y	0.25	0.27	0.29		V=3.5V
Luminance	IV	536	670	_	CD/M <sup>2</sup>	ILED=32mA
(Without LCD)						
						ILED≦32 mA
LED Lifetime	_	_	50000	_	Hr.	25□,50-60%RH
						Note1
Color	White					

Note: The LED of B/L is drive by current only; driving voltage is only for reference To make driving current in safety area (waste current between minimum and maximum).

Note1: 50000 hours is only an estimate for reference.



## 8. Reliability

Content of Reliability Test (Wide temperature, -20°C~+70°C)

	Environmental Test						
Test Item	Content of Test	Test Condition	Note				
High Temperature storage	Endurance test applying the high storage temperature for a long time.	200hrs	2				
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C 200hrs	1,2				
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	+70°C 200hrs					
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1				
High Temperature/ Humidity storage	The module should be allowed to stand at 60□,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	+60°C,90%RH 96hrs	1,2				
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation  -20°C +25°C +70°C  30min 5min 30min 1 cycle	-20°C/+70°C 10 cycles					
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude: 1.5mm  Vibration Frequency: 10~55Hz  One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3				
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(Contact), ±800v(Air), RS=330 Ω CS=150pF 10 times					

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: The packing have to including into the vibration testing.

# 9. Inspection specification

NO	Item		Criterion					
01	Electrical Testing	<ul> <li>1.1 Missing vertical, horizontal segment, segment contrast defect.</li> <li>1.2 Missing character, dot or icon.</li> <li>1.3 Display malfunction.</li> <li>1.4 No function or no display.</li> <li>1.5 Current consumption exceeds product specifications.</li> <li>1.6 LCD viewing angle defect.</li> <li>1.7 Mixed product types.</li> <li>1.8 Contrast defect.</li> </ul>				0.65		
02	Black or white spots on LCD (display only)	<ul> <li>2.1 White and black spots on display ≤0.25mm, no more than three white or black spots present.</li> <li>2.2 Densely spaced: No more than two spots or lines within 3mm</li> </ul>						
03	LCD black spots, white spots, contamination (non-display)	3.1 Round type $\Phi = (x + y) / $ $X \longrightarrow X$ 3.2 Line type : (	2 ↓ ▼ Y	SIZE $\Phi \le 0.10$ $0.10 < \Phi \le 0.20$ $0.20 < \Phi \le 0.25$ $0.25 < \Phi$	Acceptable Q TY Accept no dense 2 1 0 Acceptable Q TY Acceptable Q TY Accept no dense 2 As round type	2.5		
04	Polarizer bubbles	If bubbles are v judge using blac specifications, r to find, must cho specify direction	ck spot not easy eck in	Size Φ $ Φ \le 0.20 $ $ 0.20 < Φ \le 0.50 $ $ 0.50 < Φ \le 1.00 $ $ 1.00 < Φ $ $ Total Q TY$	Acceptable Q TY Accept no dense 3 2 0 3	2.5		

NO	Item		Criterion		AQL
05	Scratches	Follow NO.3 LCD black	spots, white spots, con	tamination	
05	Chipped glass	Symbols Define: x: Chip length y: 0 k: Seal width t: 0 L: Electrode pad length 6.1 General glass chip : 6.1.1 Chip on panel sur  z: Chip thickness  Z≤1/2t  1/2t <z≤2t 2="" 6.1.2="" are="" chip="" corner="" crack:="" if="" more="" or="" td="" there="" thickness<="" z:="" ⊙=""><td>Chip width z: Chip Glass thickness a: LCI  iface and crack between  y: Chip width  Not over viewing  area  Not exceed 1/3k  chips, x is total length of  y: Chip width</td><td>thickness D side length  n panels:  x: Chip length  x≤ 1/8a  x≤ 1/8a  of each chip.</td><td>2.5</td></z≤2t>	Chip width z: Chip Glass thickness a: LCI  iface and crack between  y: Chip width  Not over viewing  area  Not exceed 1/3k  chips, x is total length of  y: Chip width	thickness D side length  n panels:  x: Chip length  x≤ 1/8a  x≤ 1/8a  of each chip.	2.5
		Z≦1/2t	Not over viewing	x≦1/8a	
		1/2t < z ≤ 2t  ⊙ If there are 2 or more	area Not exceed 1/3k chips, x is the total leng	x≦1/8a gth of each chip.	

NO	Item	Criterion	AQL					
		Symbols:  x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length 6.2 Protrusion over terminal: 6.2.1 Chip on electrode pad:						
		y: Chip widthx: Chip lengthz: Chip thickness $y \le 0.5 \text{mm}$ $x \le 1/8 \text{a}$ $0 < z \le t$						
		6.2.2 Non-conductive portion:						
06	Glass crack	J Z X X X X	2.5					
		y: Chip width x: Chip length z: Chip thickness						
		$y \le L$ $x \le 1/8a$ $0 < z \le t$						
		⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO						
	must remain and be inspected according to electrode terminal specifications.  ⊙ If the product will be heat sealed by the customer, the alignment mark not be damaged.							
		6.2.3 Substrate protuberance and internal crack.						
		y: width x: length						
		$y \le 1/3L$ $x \le a$						

NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	<ul> <li>8.1 Illumination source flickers when lit.</li> <li>8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards.</li> <li>8.3 Backlight doesn't light or color wrong.</li> </ul>	0.65 2.5 0.65
09	Bezel	<ul><li>9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.</li><li>9.2 Bezel must comply with job specifications.</li></ul>	2.5 0.65
10	PCB · COB	<ul> <li>10.1 COB seal may not have pinholes larger than 0.2mm or contamination.</li> <li>10.2 COB seal surface may not have pinholes through to the IC.</li> <li>10.3 The height of the COB should not exceed the height indicated in the assembly diagram.</li> <li>10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places.</li> <li>10.5 No oxidation or contamination PCB terminals.</li> <li>10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts.</li> <li>10.7 The jumper on the PCB should conform to the product characteristic chart.</li> <li>10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down.</li> <li>10.9 The Scraping testing standard for Copper Coating of PCB</li> </ul>	2.5 2.5 0.65 2.5 0.65 2.5 2.5
11	Soldering	<ul> <li>11.1 No un-melted solder paste may be present on the PCB.</li> <li>11.2 No cold solder joints, missing solder connections, oxidation or icicle.</li> <li>11.3 No residue or solder balls on PCB.</li> <li>11.4 No short circuits in components on PCB.</li> </ul>	<ul><li>2.5</li><li>2.5</li><li>2.5</li><li>0.65</li></ul>

NO	Item	Criterion	AQL
	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface	2.5
		pin must be present or look as if it cause the interface pin to	
		sever.	2.5
12		12.6 The residual rosin or tin oil of soldering (component or chip	
		component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	0.65
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on	
		packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	0.03
		12.12 Visual defect outside of VA is not considered to be rejection.	

#### 10. Precautions in use of LCD Modules

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) DISPLAY have the right to change the passive components, including R3,R6 & backlight adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- (9) DISPLAY have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, DISPLAY have the right to modify the version.)
- (10) To ensure the stability of the display screen, please apply screen saver after showing 30 minutes of fixed display content.