

Display Elektronik GmbH

# DATA SHEET

***TFT- MODULE***

**DEM 168384A VRH-FW-N**

**2,9" refl. TFT + Frontlight**

Product Specification

Ver.: 2

01.03.2024

**Revision History**

<b>Revision</b>	<b>Date</b>	<b>Originator</b>	<b>Detail</b>	<b>Remarks</b>
0	20.12.2023	LQ	Initial Release	-
1	26.02.2024	LL	Modify Weight Modify Current Consumption All Black Modify Optical Characteristics	P4 P5 P6
2	01.03.2024	LL	Modify Backlight to Frontlight	P5, P9

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**1. General Description**

The specification is a reflective type color active matrix liquid crystal display (LCD) which uses amorphous thin film transistor (TFT) as switching devices. This product is composed of a TFT-LCD panel, driver ICs and a Frontlight unit.

**2. Module Parameter**

Features	Details	Unit
Display Size(Diagonal)	2.9"	-
Display Mode	Reflective /Normally White	-
Resolution	168 x 384	Pixels
View Direction	Full view	Best Image
Module Outline	41.20 x 76.05 x 2.18 ( Note1 )	mm
Active Area	29.10 x 66.85	mm
Pixel Size	0.1732 x 0.17408	mm
Polarizer Surface Treatment	Anti-Glare	-
Color	BWR	-
Interface	4-line serial interface	-
With or without Touch Panel	Without	-
Driver IC	ST7306 (Sitronix)	-
Operating Temperature	-20°C to +70°C	°C
Storage Temperature	-30°C to +80°C	°C
Weight	12	G

Note 1: Exclusive hooks, posts, FFC/FPC tail etc.

**3. Absolute Maximum Ratings**

VSS=0V, Ta=25°C

Item	Symbol	Min.	Max.	Unit
Supply Voltage	VCI	-0.3	4.0	V
	IOVCC	-0.3	4.0	V
Storage temperature	T <sub>STG</sub>	-30	+80	°C
Operating temperature	T <sub>OP</sub>	-20	+70	°C

Note 1: If Ta below 50°C, the maximal humidity is 90%RH, if Ta over +50°C, absolute humidity should be less than 60%RH.

Note 2: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

4. DC Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	V <sub>CI</sub>	2.55	3.0	3.6	V
	IOVCC	1.65	3.0	3.6	V
Logic Low Input Voltage	V <sub>IL</sub>	GND	-	0.3*IOVCC	V
Logic High Input Voltage	V <sub>IH</sub>	0.7*IOVCC	-	IOVCC	V
Logic Low Output Voltage	V <sub>OL</sub>	GND	-	0.2*IOVCC	V
Logic High Output Voltage	V <sub>OH</sub>	0.8*IOVCC	-	IOVCC	V
Current Consumption All Black	I <sub>CI</sub>	-	0.3	-	mA

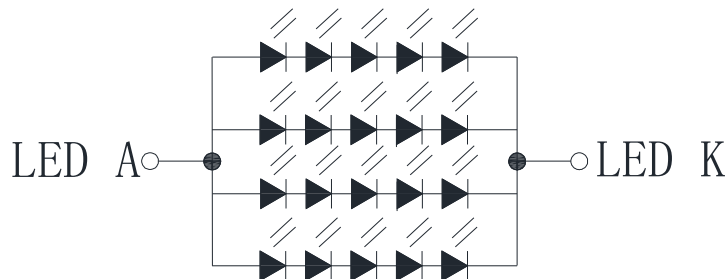
5. Frontlight Characteristic

5.1. Frontlight Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Forward Voltage	V <sub>F</sub>	T <sub>a</sub> =25 °C, I <sub>F</sub> =20mA/LED	14.0	16.0	17.0	V
Forward Current	I <sub>F</sub>	T <sub>a</sub> =25 °C, V <sub>F</sub> =3.2V/LED	-	80	-	mA
Power dissipation	P <sub>D</sub>		-	1280	-	mW
LED Life Time(25 °C)	-	-	20,000	30,000	-	hrs
Uniformity	Avg	-		80	-	%
Drive method	Constant current					
LED Configuration	20 White LEDs(5 LEDs in one string and 4 groups in parallel)					

Note1: LED life time defined as follows: The final brightness is at 50% of original brightness.  
The environmental conducted under ambient air flow, at T<sub>a</sub>=25°C±2° C, 60%RH±5%, I<sub>F</sub>=20m A/LED.

5.2. Frontlighting Circuit



6. Optical Characteristics

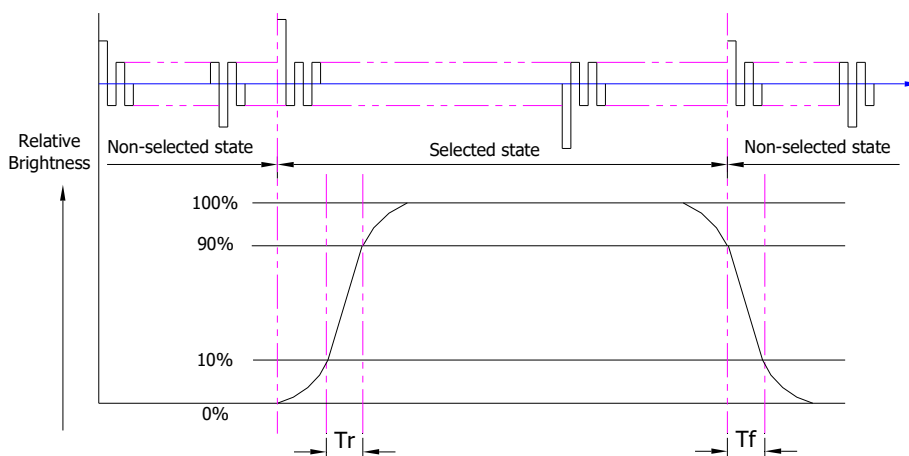
6.1. Optical Characteristics

Ta=25°C, VCI=3.0V

Item	Symbol	Condition	Specification			Unit
			Min.	Typ.	Max.	
Luminance on TFT( $I_f=20\text{mA/LED}$ )	Lv	Normally viewing angle $\theta_x = \phi_y = 0^\circ$	250	310	-	cd/m <sup>2</sup>
Contrast ratio(See 6.3)	CR		-	15	-	-
Response time (See 6.2)	TR+TF		-	5	7	ms
Chromaticity Transmissive (See 6.5)	Red	XR	0.299	0.349	0.339	-
		YR	0.264	0.314	0.364	-
	Green	XG	-	-	-	-
		YG	-	-	-	-
	Blue	XB	-	-	-	-
		YB	-	-	-	-
White	XW	0.256	0.306	0.356	-	
	YW	0.280	0.330	0.380	-	
Viewing Angle (See 6.4)	Horizontal	$\theta_{x+}$	-	60	-	Deg.
		$\theta_{x-}$	-	60	-	
	Vertical	$\phi_{y+}$	-	60	-	
		$\phi_{y-}$	-	60	-	

6.2. Definition of Response Time

6.2.1. Normally Black Type (Negative)

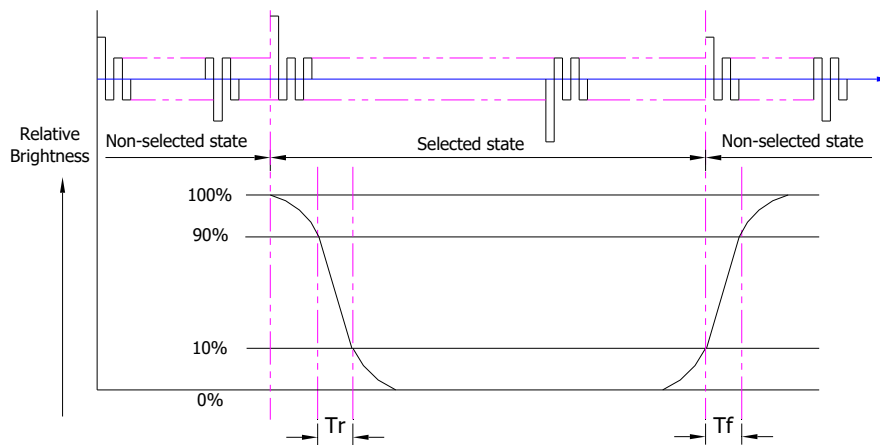


Tr is the time it takes to change from non-selected state with relative luminance 10% to selected state with relative luminance 90%;

Tf is the time it takes to change from selected state with relative luminance 90% to non-selected state with relative luminance 10%.

Note : Measuring machine: LCD-5100

6.2.2. Normally White Type (Positive)



Tr is the time it takes to change from non-selected stage with relative luminance 90% to selected state with relative luminance 10%;

Tf is the time it takes to change from selected state with relative luminance 10% to non-selected state with relative luminance 90%;

Note: Measuring machine: LCD-5100 or EQUI

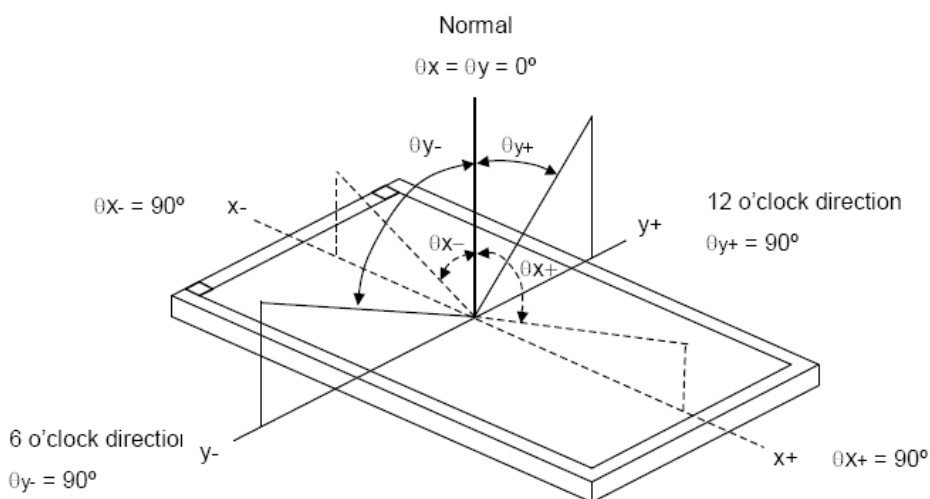
6.3. Definition of Contrast Ratio

Contrast is measured perpendicular to display surface in reflective and transmissive mode. The measurement condition is:

Measuring Equipment	Eldim or Equivalent
Measuring Point Diameter	3mm//1mm
Measuring Point Location	Active Area centre point
Test pattern	A: All Pixels white B: All Pixel black
Contrast setting	Maximum

Definitions: CR (Contrast) = Luminance of White Pixel / Luminance of Black Pixel

6.4. Definition of Viewing Angles

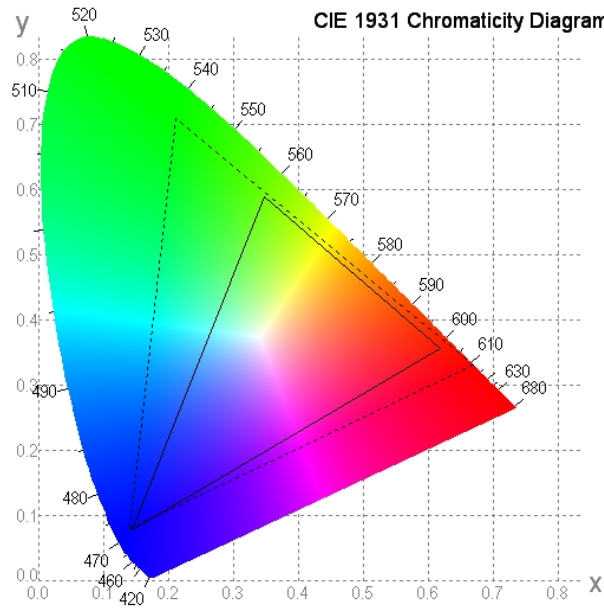


Measuring machine: LCD-5100 or EQUI

6.5. Definition of Color Appearance

R,G,B and W are defined by (x, y) on the IE chromaticity diagram  
 NTSC=area of RGB triangle/area of NTSC triangleX100%

Measuring picture: Red, Green, Blue and White (Measuring machine: BM-7)

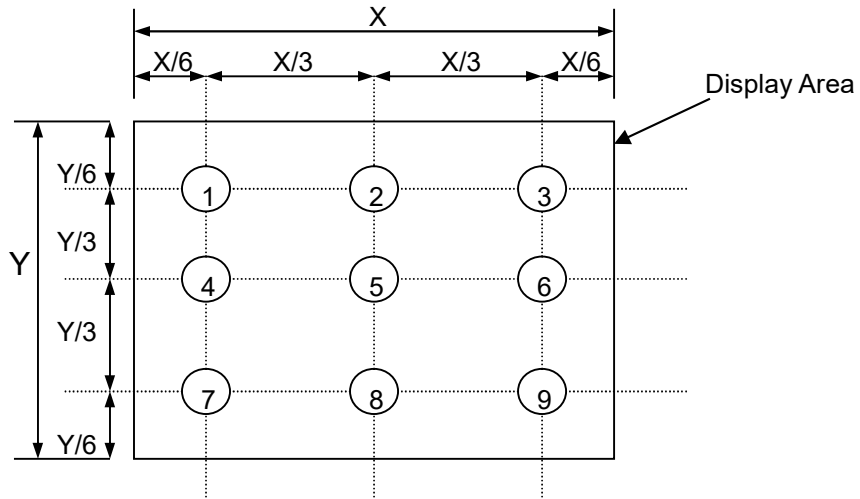


**6.6. Definition of Surface Luminance, Uniformity and Transmittance**

Using the reflective mode measurement approach, measure the white screen luminance of the display panel and Frontlight.

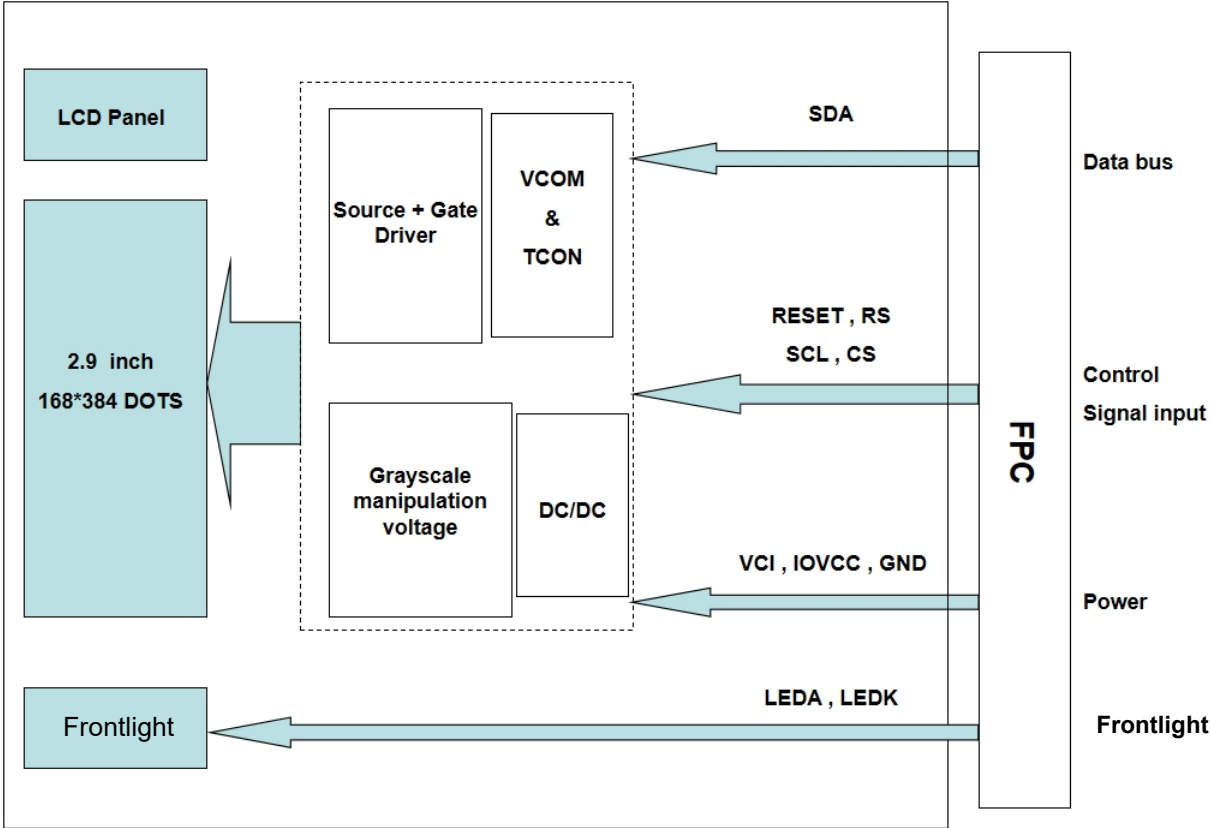
- 6.6.1. Surface Luminance:  $L_V = \text{average} (L_{P1}:L_{P9})$
- 6.6.2. Uniformity =  $\text{Minimal} (L_{P1}:L_{P9}) / \text{Maximal} (L_{P1}:L_{P9}) * 100\%$
- 6.6.3. Transmittance =  $L_V \text{ on LCD} / L_V \text{ on Frontlight} * 100\%$

Note: Measuring machine: BM-7





7. Block Diagram and Power Supply

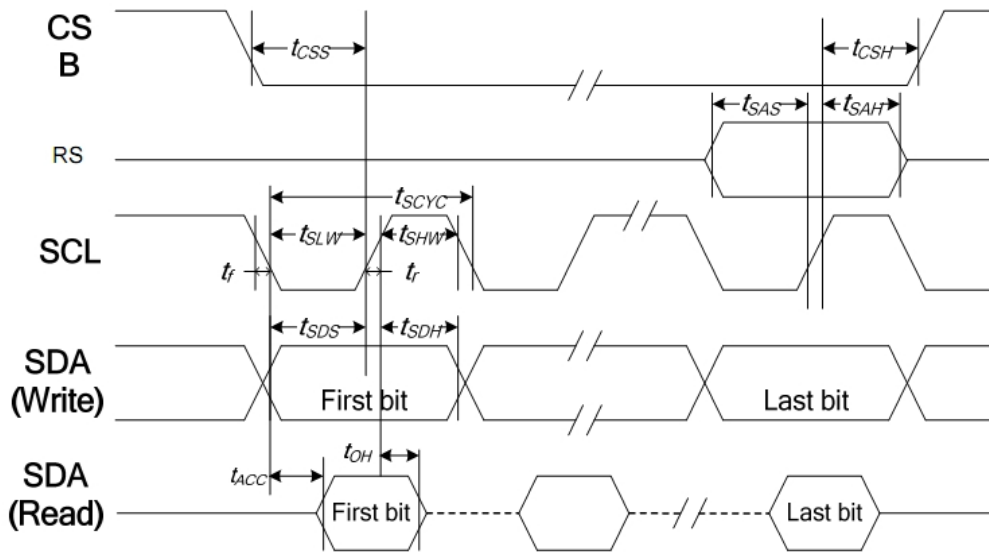


**8. Interface Pins Definition**

No.	Symbol	Function
1	LEDA	Frontlight Anode.
2	LEDK	Frontlight Cathode.
3	GND	Ground.
4	GND	Ground.
5	VCI	Power Supply(Analog)
6	IOVCC	Power Supply(Digital)
7	GND	Ground.
8	RESET	Reset input pin.When RSTB is "L".
9	VPP	The programming power supply of the built-in NVM.Apply external power 7.5V here when programming(>8mA for successful programming). If not used,left this pin open.
10	RS	Data or Command Signal. "L" are control data; "H" are display data;
11	SCL	Serial input clock.
12	SDA	Serial input data.
13	CS	Chip select input pin.
14	GND	Ground.
15	GND	Ground.

9. AC Characteristics

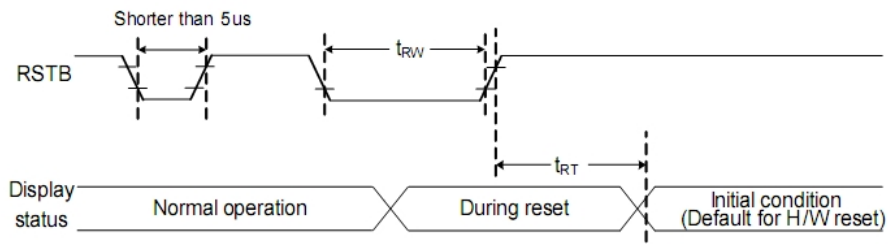
9.1. 4SPI Interface Characteristics



IOVCC= 1.8~3.3V, Ta=25 °C

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period (Write)	SCL	tSCYC		30	—	ns
Serial clock period (Read)				150		
SCLK "H" pulse width (Write)		tSHW		15	—	
SCLK "H" pulse width (Read)				60		
SCLK "L" pulse width (Write)		tSLW		15	—	
SCLK "L" pulse width (Read)				60		
Address setup time	RS	tSAS	10	—		
Address hold time		tSAH	10	—		
Data setup time	SDA	tSDS	10	—		
Data hold time	(Write)	tSDH	10	—		
Read data access time	SDA	tACC	For maximum CL=30p	10	50	
Read data output disable time	(Read)	tOH	For minimum CL=8p	15	50	
CSB-SCLK time	CSB	tCSS	10	—		
CSB-SCLK time		tCSH	10	—		

9.2. Reset Timing



IOVCC = 1.8~3.3V, Ta=25 °C

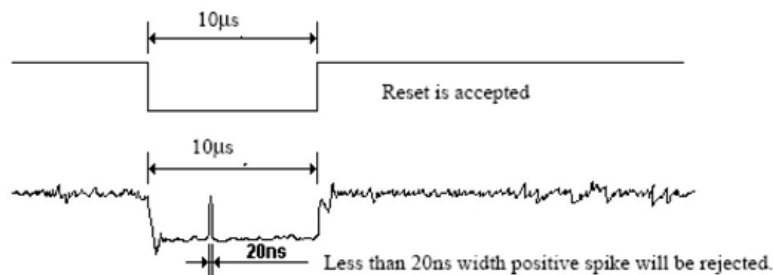
Item	Symbol	Condition	Min.	Max.	Unit
Reset "L" pulse width	tRW		1	—	ms
Reset cancel	tRT	Note1, 5 (sleep-in mode)	—	5	ms
		Note1, 6, 7 (sleep-out mode)	—	120	ms

Notes:

- The reset cancel includes also required time for loading ID bytes, VSource setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RSTB.
- Spike due to an electrostatic discharge on RSTB line does not cause irregular system reset according to the table below:

RSTB Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

- During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.
- Spike Rejection also applies during a valid reset pulse as shown below:



- When Reset applied during Sleep In Mode.
- When Reset applied during Sleep Out Mode.
- It is necessary to wait 5msec after releasing RSTB before sending commands. Also Sleep Out command cannot be sent for 120msec.

## **10. Quality Assurance**

### **10.1.Purpose**

This standard for Quality Assurance assures the quality of LCD module products supplied to customer.

### **10.2.Standard for Quality Test**

#### 10.2.1. Sampling Plan:

GB2828.1-2012

Single sampling, general inspection level II

#### 10.2.2. Sampling Criteria:

Visual inspection: AQL 1.5

Electrical functional: AQL 0.65.

#### 10.2.3. Reliability Test:

Detailed requirement refer to Reliability Test Specification.

### **10.3.Nonconforming Analysis & Disposition**

#### 10.3.1. Nonconforming analysis:

10.3.1.1. Customer should provide overall information of non-conforming sample for their complaints.

10.3.1.2. After receipt of detailed information from customer, the analysis of nonconforming parts usually should be finished in one week.

10.3.1.3. If cannot finish the analysis on time, customer will be notified with the progress status.

#### 10.3.2. Disposition of nonconforming:

10.3.2.1. Non-conforming product over PPM level will be replaced.

10.3.2.2. The cause of non-conformance will be analyzed. Corrective action will be discussed and implemented.

### **10.4.Agreement Items**

Shall negotiate with customer if the following situation occurs:

10.4.1. There is any discrepancy in standard of quality assurance.

10.4.2. Additional requirement to be added in product specification.

10.4.3. Any other special problem.

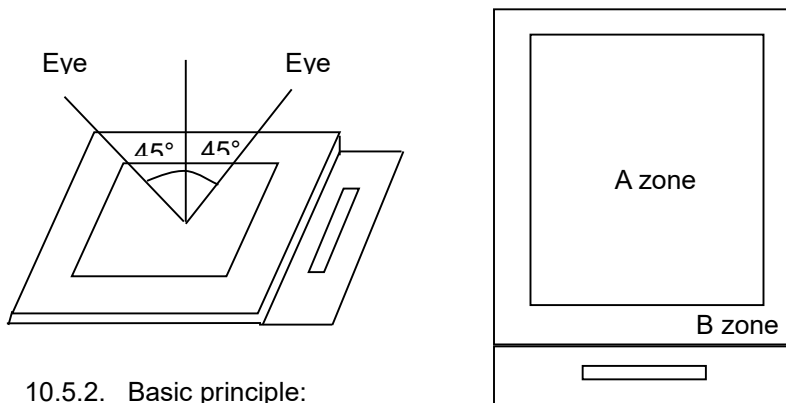
### **10.5.Standard of the Product Visual Inspection**

#### 10.5.1. Appearance inspection:

10.5.1.1. The inspection must be under illumination about 1000 – 1500 lx, and the distance of view must be at 30cm ± 2cm.

10.5.1.2. The viewing angle should be 45° from the vertical line without reflection light or follows customer's viewing angle specifications.

10.5.1.3. Definition of area: A Zone: Active Area, B Zone: Viewing Area,

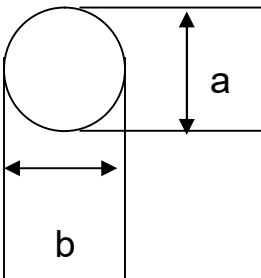


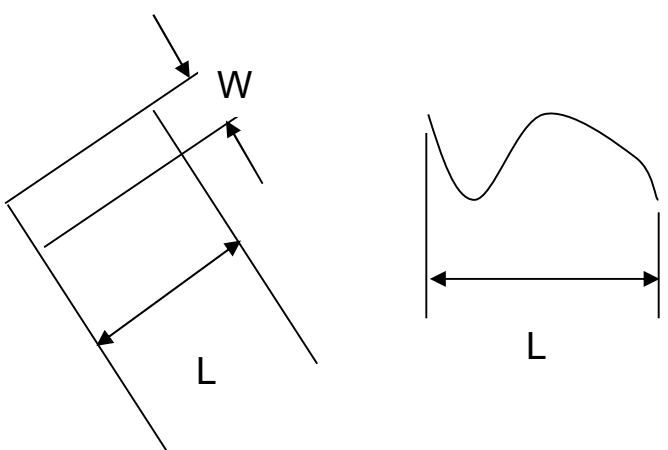
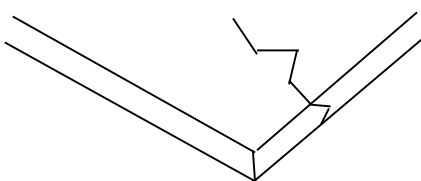
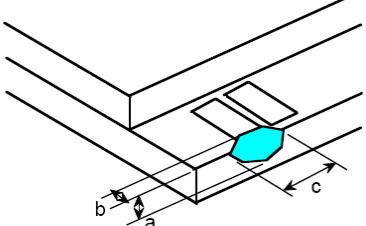
10.5.2. Basic principle:

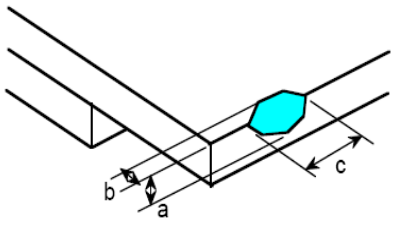
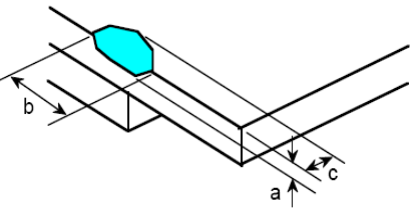
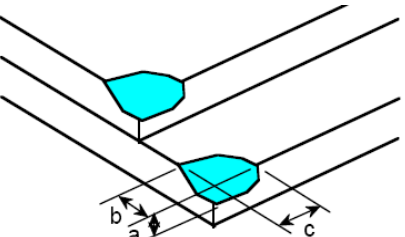
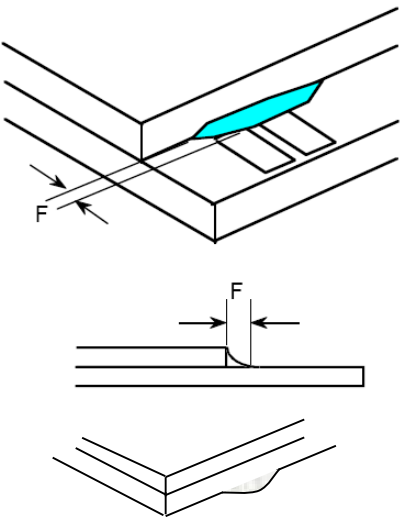
10.5.2.1. A set of sample to indicate the limit of acceptable quality level must be discussed by both us and customer when there is any dispute happened.

10.5.2.2. New item must be added on time when it is necessary.

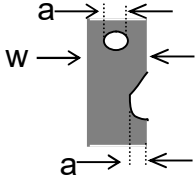
10.6. Inspection Specification

No.	Item	Criteria (Unit: mm)																	
01	Black / White spot Foreign material (Round type) Pinholes Stain Particles inside cell. (Minor defect)	 <table border="1" data-bbox="916 972 1417 1317"> <thead> <tr> <th>Size \ Area</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>\phi \leq 0.10</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.10 &lt; \phi \leq 0.15</math></td> <td>2</td> </tr> <tr> <td><math>0.15 &lt; \phi \leq 0.25</math></td> <td>1</td> </tr> <tr> <td><math>0.25 &lt; \phi</math></td> <td>0</td> </tr> <tr> <td>Total</td> <td>2 no include <math>\phi \leq 0.10</math></td> </tr> </tbody> </table> <p><math>\phi = (a + b) / 2</math> Distance between 2 defects should more than 3mm apart.</p>	Size \ Area	Acc. Qty	$\phi \leq 0.10$	Ignore	$0.10 < \phi \leq 0.15$	2	$0.15 < \phi \leq 0.25$	1	$0.25 < \phi$	0	Total	2 no include $\phi \leq 0.10$					
Size \ Area	Acc. Qty																		
$\phi \leq 0.10$	Ignore																		
$0.10 < \phi \leq 0.15$	2																		
$0.15 < \phi \leq 0.25$	1																		
$0.25 < \phi$	0																		
Total	2 no include $\phi \leq 0.10$																		
02	Electrical Defect (Minor defect)	<table border="1" data-bbox="523 1482 1347 1697"> <thead> <tr> <th></th> <th>Display Area</th> <th>Total</th> <th rowspan="3">Note1</th> </tr> </thead> <tbody> <tr> <td>Bright dot</td> <td>0</td> <td>0</td> </tr> <tr> <td>Dark dot</td> <td><math>N \leq 2</math></td> <td><math>N \leq 2</math></td> </tr> <tr> <td>Total dot</td> <td><math>N \leq 2</math></td> <td><math>N \leq 2</math></td> <td rowspan="2">Note2</td> </tr> <tr> <td>Mura</td> <td colspan="2">Not visible through 5% ND filters.</td> </tr> </tbody> </table> <p>Remark: 1. Bright dot caused by scratch and foreign object accords to item 1.</p>		Display Area	Total	Note1	Bright dot	0	0	Dark dot	$N \leq 2$	$N \leq 2$	Total dot	$N \leq 2$	$N \leq 2$	Note2	Mura	Not visible through 5% ND filters.	
	Display Area	Total	Note1																
Bright dot	0	0																	
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Total dot	$N \leq 2$	$N \leq 2$	Note2																
Mura	Not visible through 5% ND filters.																		

<p>03</p>	<p>Black and White line Scratch Foreign material (Line type) (Minor defect)</p>	 <table border="1" data-bbox="582 716 1204 1030"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td>/</td> <td><math>W \leq 0.03</math></td> <td>Ignore</td> </tr> <tr> <td><math>L \leq 2.5</math></td> <td><math>0.03 &lt; W \leq 0.05</math></td> <td>3</td> </tr> <tr> <td><math>L \leq 2.5</math></td> <td><math>0.05 &lt; W \leq 0.10</math></td> <td>2</td> </tr> <tr> <td>/</td> <td><math>0.1 &lt; W</math></td> <td></td> </tr> <tr> <td colspan="2">Total</td> <td>3</td> </tr> </tbody> </table> <p>Distance between 2 defects should more than 3mm apart. Scratches not viewable through the back of the display are acceptable.</p>	Length	Width	Acc. Qty	/	$W \leq 0.03$	Ignore	$L \leq 2.5$	$0.03 < W \leq 0.05$	3	$L \leq 2.5$	$0.05 < W \leq 0.10$	2	/	$0.1 < W$		Total		3
Length	Width	Acc. Qty																		
/	$W \leq 0.03$	Ignore																		
$L \leq 2.5$	$0.03 < W \leq 0.05$	3																		
$L \leq 2.5$	$0.05 < W \leq 0.10$	2																		
/	$0.1 < W$																			
Total		3																		
<p>04</p>	<p>Glass Crack (Minor defect)</p>	 <p>Crack is potential to enlarge, any type is not allowed.</p>																		
<p>05</p>	<p>Glass Chipping Pad Area: (Minor defect)</p> 	<table border="1" data-bbox="869 1568 1340 1736"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td> <td>1</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td> <td>3</td> </tr> <tr> <td colspan="2"><math>a &lt; \text{Glass Thickness}</math></td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	3	$a < \text{Glass Thickness}$											
Length and Width	Acc. Qty																			
$c > 3.0, b < 1.0$	1																			
$c < 3.0, b < 1.0$	3																			
$a < \text{Glass Thickness}$																				

<p>06</p>	<p>Glass Chipping Rear of Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td> <td>1</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td> <td>2</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 0.5</math></td> <td>4</td> </tr> <tr> <td colspan="2">a &lt; Glass Thickness</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	a < Glass Thickness	
Length and Width	Acc. Qty											
$c > 3.0, b < 1.0$	1											
$c < 3.0, b < 1.0$	2											
$c < 3.0, b < 0.5$	4											
a < Glass Thickness												
<p>07</p>	<p>Glass Chipping Except Pad Area: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &gt; 3.0, b &lt; 1.0</math></td> <td>1</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 1.0</math></td> <td>2</td> </tr> <tr> <td><math>c &lt; 3.0, b &lt; 0.5</math></td> <td>4</td> </tr> <tr> <td colspan="2">a &lt; Glass Thickness</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c > 3.0, b < 1.0$	1	$c < 3.0, b < 1.0$	2	$c < 3.0, b < 0.5$	4	a < Glass Thickness	
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<p>08</p>	<p>Glass Corner Chipping: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length and Width</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>c &lt; 3.0, b &lt; 3.0</math></td> <td>Ignore</td> </tr> <tr> <td colspan="2">a &lt; Glass Thickness</td> </tr> </tbody> </table>	Length and Width	Acc. Qty	$c < 3.0, b < 3.0$	Ignore	a < Glass Thickness					
Length and Width	Acc. Qty											
$c < 3.0, b < 3.0$	Ignore											
a < Glass Thickness												
<p>09</p>	<p>Glass Burr: (Minor defect)</p> 	<table border="1"> <thead> <tr> <th>Length</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>F &lt; 1.0</math></td> <td>Ignore</td> </tr> </tbody> </table> <p>Glass burr don't affect assemble and module dimension.</p>	Length	Acc. Qty	$F < 1.0$	Ignore						
Length	Acc. Qty											
$F < 1.0$	Ignore											



10	<p>FPC Defect: (Minor defect)</p> 	<p>10.1 Dent, pinhole width <math>a &lt; w/3</math>. (w: circuitry width.) 10.2 Open circuit is unacceptable. 10.3 No oxidation, contamination and distortion.</p>										
11	<p>Bubble on Polarizer (Minor defect)</p>	<table border="1"> <thead> <tr> <th>Diameter</th> <th>Acc. Qty</th> </tr> </thead> <tbody> <tr> <td><math>\varphi \leq 0.20</math></td> <td>Ignore</td> </tr> <tr> <td><math>0.20 &lt; \varphi \leq 0.30</math></td> <td>4</td> </tr> <tr> <td><math>0.30 &lt; \varphi \leq 0.50</math></td> <td>1</td> </tr> <tr> <td><math>0.50 &lt; \varphi</math></td> <td>None</td> </tr> </tbody> </table>	Diameter	Acc. Qty	$\varphi \leq 0.20$	Ignore	$0.20 < \varphi \leq 0.30$	4	$0.30 < \varphi \leq 0.50$	1	$0.50 < \varphi$	None
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13	<p>Bezel</p>	<p>13.1 No rust, distortion on the Bezel. 13.2 No visible fingerprints, stains or other contamination.</p>										
14	<p>PCB</p>	<p>14.1 No distortion or contamination on PCB terminals. 14.2 All components on PCB must same as documented on the BOM/component layout. 14.3 Follow IPC-A-600F.</p>										
15	<p>Soldering</p>	<p>Follow IPC-A-610C standard</p>										
16	<p>Electrical Defect (Major defect)</p>	<p>The below defects must be rejected. 16.1 Missing vertical / horizontal segment, 16.2 Abnormal Display. 16.3 No function or no display. 16.4 Current exceeds product specifications. 16.5 LCD viewing angle defect. 16.6 No Frontlight. 16.7 Dark Frontlight. 16.8 Touch Panel no function.</p>										

Remark: LCD Panel Broken shall be rejected. Defect out of LCD viewing area is acceptable.

**10.7. Classification of Defects**

- 10.7.1. Visual defects (Except no / wrong label) are treated as minor defect and electrical defect is major.
- 10.7.2. Two minor defects are equal to one major in lot sampling inspection.

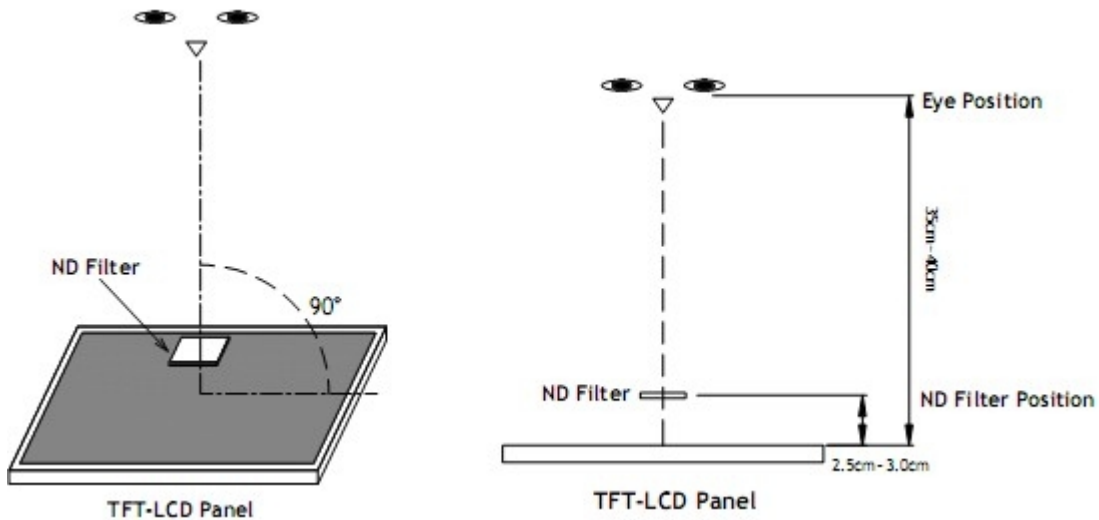
**10.8. Identification/marketing criteria**

Any unit with illegible / wrong /double or no marking/ label shall be rejected.

**10.9. Packaging**

- 10.9.1. There should be no damage of the outside carton box, each packaging box should have one identical label.
- 10.9.2. Modules inside package box should have compliant mark.
- 10.9.3. All direct package materials shall offer ESD protection.

**Note1:** Bright dot is defined as the defective area of the dot is larger than 50% of one sub-pixel area.



Bright dot: The bright dot size defect at black display pattern. It can be recognized by 2% transparency of filter when the distance between eyes and panel is  $350\text{mm} \pm 50\text{mm}$ .

Dark dot: Cyan, Magenta or Yellow dot size defect at white display pattern. It can be recognized by 5% transparency of filter when the distance between eyes and panel is  $350\text{mm} \pm 50\text{mm}$ .

**Note2:** Mura on display which appears darker / brighter against background brightness on parts of display area.

**11. Reliability Specification**

No	Item	Condition	Quantity	Criteria
1	High Temperature Operating	+70°C, 96Hrs	2	GB/T2423.2 -2008
2	Low Temperature Operating	-20°C, 96Hrs	2	GB/T2423.1 -2008
3	High Humidity Storage	+50°C, 90%RH, 96Hrs	2	GB/T2423.3 -2016
4	High Temperature Storage	+80°C, 96Hrs	2	GB/T2423.2 -2008
5	Low Temperature Storage	-30°C, 96Hrs	2	GB/T2423.1 -2008
6	Thermal Cycling Test Storage	-20°C, 60min~+70°C, 60min, 20 cycles.	2	GB/T2423.22 -2012
7	Packing vibration	Frequency range:10Hz~50Hz Acceleration of gravity:5G X, Y, Z 30 min for each direction.	-	GB/T5170.14 -2009
8	Electrical Static Discharge	Air: ±8kV 150pF/330 Ω 5 times Contact: ±4kV 150pF/330 Ω 5 times	2	GB/T17626.2 -2018
9	Drop Test (Packaged)	Height:80 cm,1 corner, 3 edges, 6 surfaces.	-	GB/T2423.8 -1995

Note1. No deflection cosmetic and operational function allowable.

Note2. Total current Consumption should be below double of initial value.

**12. Precautions and Warranty**

**12.1. Safety**

- 12.1.1. The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.
- 12.1.2. Since the liquid crystal cells are made of glass, do not apply strong impact on them. Handle with care.

**12.2. Handling**

- 12.2.1. Reverse and use within ratings in order to keep performance and prevent damage.
- 12.2.2. Do not wipe the polarizer with dry cloth, as it might cause scratch. If the surface of the LCD needs to be cleaned, wipe it swiftly with cotton or other soft cloth soaked with petroleum IPA, do not use other chemicals.

**12.3. Storage**

- 12.3.1. Do not store the LCD module beyond the specified temperature ranges.
- 12.3.2. Strong light exposure causes degradation of polarizer and color filter.

**12.4. Metal Pin (Apply to Products with Metal Pins)**

- 12.4.1. Pins of LCD and Frontlight
  - 12.4.1.1. Solder tip can touch and press on the tip of Pin LEAD during the soldering

12.4.1.2. Recommended Soldering Conditions

Solder Type: Sn96.3~94-Ag3.3~4.3-Cu0.4~1.1

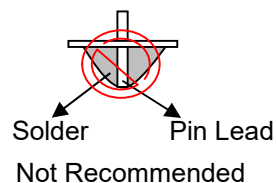
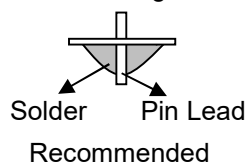
Maximum Solder Temperature: 370°C

Maximum Solder Time: 3s at the maximum temperature

Recommended Soldering Temp: 350±20°C

Typical Soldering Time: ≤3s

12.4.1.3. Solder Wetting



12.4.2. Pins of EL

- 12.4.2.1. Solder tip can touch and press on the tip of EL leads during soldering.
- 12.4.2.2. No Solder Paste on the soldering pad on the motherboard is recommended.

12.4.2.3. Recommended Soldering Conditions

Solder type: Nippon Alimit Leadfree SR-34, size 0.5mm

Recommended Solder Temperature: 270~290°C

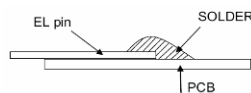
Typical Soldering Time: ≤2s

Minimum solder distance from EL lamp (body):2.0mm

- 12.4.2.4. No horizontal press on the EL leads during soldering.

- 12.4.2.5. 180° bend EL leads three times is not allowed.

12.4.2.6. Solder Wetting

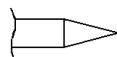


Recommended

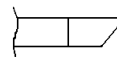


Not Recommended

12.4.2.7. The type of the solder iron:

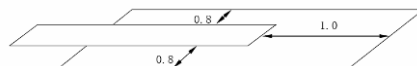


Recommended



Not Recommended

12.4.2.8. Solder Pad



**12.5.Operation**

- 12.5.1. Do not drive LCD with DC voltage
- 12.5.2. Response time will increase below lower temperature
- 12.5.3. Display may change color with different temperature
- 12.5.4. Mechanical disturbance during operation, such as pressing on the display area, may cause the segments to appear “fractured”.
- 12.5.5. Do not connect or disconnect the LCM to or from the system when power is on.
- 12.5.6. Never use the LCM under abnormal condition of high temperature and high humidity.
- 12.5.7. Module has high frequency circuits. Sufficient suppression to the electromagnetic interface shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- 12.5.8. *Do not display the fixed pattern for long time (we suggest the time not longer than one hour) because it will develop image sticking due to the TFT structure.*

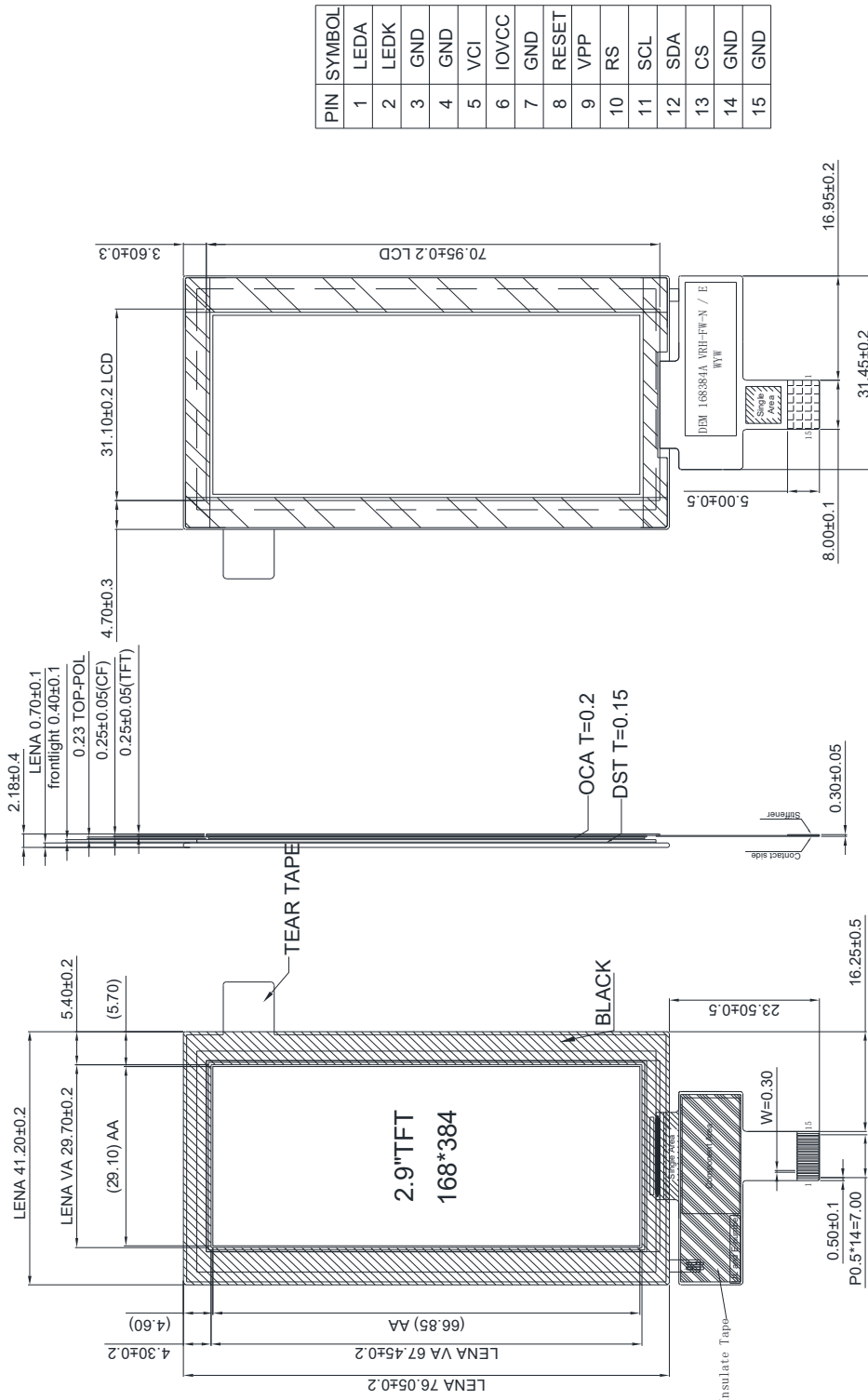
**12.6.Static Electricity**

- 12.6.1. CMOS LSIs are equipped in this unit, so care must be taken to avoid the electro-static charge, by ground human body, etc.
- 12.6.2. The normal static prevention measures should be observed for work clothes and benches.
- 12.6.3. The module should be kept into anti-static bags or other containers resistant to static for storage.

**12.7.Limited Warranty**

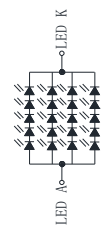
- 12.7.1. Our warranty liability is limited to repair and/or replacement. We will not be responsible for any consequential loss.
- 12.7.2. If possible, we suggest customer to use up all modules in six months. If the module storage time over twelve months, we suggest that recheck it before the module be used
- 12.7.3. . After the product shipped, any product quality issues must be feedback within three months, otherwise, we will not be responsible for the subsequent or consequential events.

13. Outline Drawing



PIN	SYMBOL
1	LEDA
2	LEDK
3	GND
4	GND
5	VCI
6	IOVCC
7	GND
8	RESET
9	VPP
10	RS
11	SCL
12	SDA
13	CS
14	GND
15	GND

- NOTES:
1. Display size: 2.9" TFT
  2. Display mode: Reflective/Normally White/Anti-glare
  3. Operation temperature: -20°C ~ +70°C
  4. Storage temperature: -30°C ~ +80°C
  5. Driver IC: ST7306
  6. Frontlight: White (20 LED) / 16V / 80mA
  7. Power supply voltage: 3.0V
  8. ROHS must be complied
- \* Unspecification tolerance are ±0.2mm



LED life: 30000h (Typ), 20000h (Min)