## DISPLAY Elektronik GmbH

# DATA SHEET

## LCD MODULE

## **DEM 20232 SYH-LY-CYR22**

**Product Specification** 

Version: 4

## **GENERAL SPECIFICATION**

## MODULE NO.:

## DEM 20232 SYH-LY-CYR22

### **CUSTOMER P/N**

VERSION NO.	CHANGE DESCRIPTION	DATE
0	ORIGINAL VERSION	09.11.2000
1	CHANGE CONTENTS	16.11.2000
2	ADD S6A0069-22	07.03.2001
3	ADD HOLES	08.08.2001
3.1.3	CHANGE IC	19.09.2008
4	Change the VDD from -0.3~+7.0 V to -0.3~+6V;VLCD from 3.0~13V to 3.0~7V in page7	11.01.2018

PREPARED BY: PS DATE: 11.01.2018

APPROVED BY: MHO DATE: 11.01.2018

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### 1.FUNCTIONS & FEATURES

MODULE NAME	LCD TYPE						
DEM 20232 SYH-LY-CYR22	STN Yellow Green Transflective Positive Mode						

• Viewing Direction : 6 o'clock

• Driving Scheme : 1/16 Duty Cycle, 1/5 Bias

• Power Supply Voltage : 5 Volt (typ.)

• Backlight Color : LED, Lightbox, Yellow Green

• VLCD Adjustable For Best Contrast : 4.5 Volt (typ.)

• Display contents : 20 x 2 Characters (5 x 8 dots, Format: 208 Kinds)

• Internal Memory : CGROM (13,200 Bits)

: CGRAM (64 x 8 Bits)

: DDRAM (80 x 8 Bits for Digits)

CGROM
 : CGROM of the ST7066-0T (English-Cyrillic)
 Intarface
 : Easy Interface with a 4-bit or 8-bit MPU

• Operating Temperature : -20°C to +70°C

• Storage Temperature :  $-30^{\circ}$ C to  $+80^{\circ}$ C

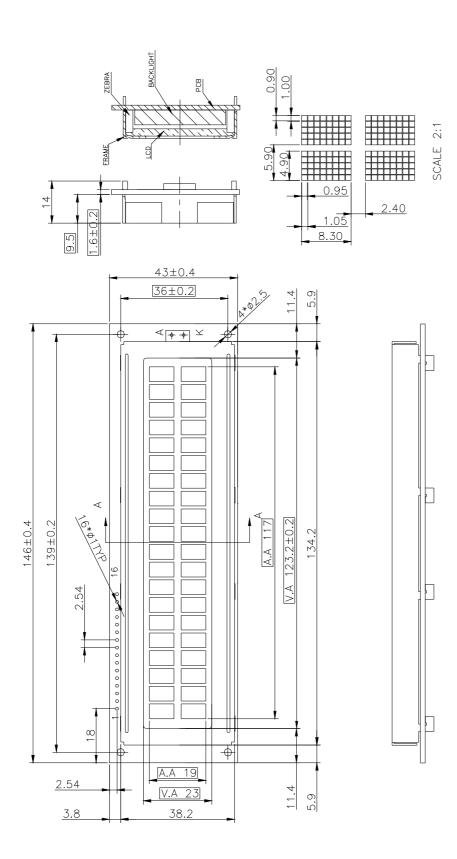
### 2. MECHANICAL SPECIFICATIONS

• Module Size : 146.00 x 43.00 x 14.00 mm

Character Pitch : 5.90 x 10.70 mm
 Character Size : 4.90 x 8.30 mm
 Character Font : 5 x 8 dots

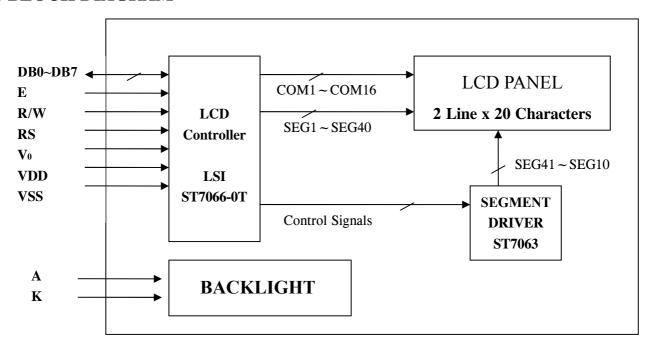
Dot Size : 0.90 x 0.95 mm
 Dot Pitch : 1.00 x 1.05 mm

### 3. EXTERNAL DIMENSIONS.



REMAKS: 1.UNMARKED TOLERANCE IS ±0.3; 2.ALL MATERIAL COMPLY WITH ROHS.

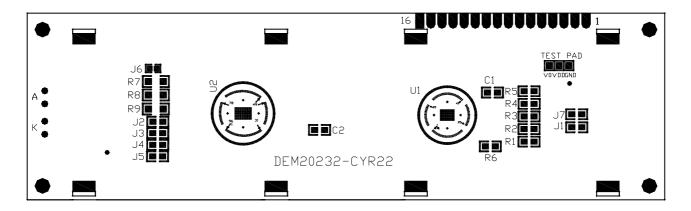
### 4. BLOCK DIAGRAM



### **5. PIN ASSIGNMENT**

Pin No.	Symbol	Function							
1	Vss	Ground							
2	V <sub>DD</sub>	Power supply							
3	V <sub>0</sub>	Power Supply for LCD							
4	RS	Select Display Data("H") or Instructions("L")							
5	R/W	Read or Write Select Signal							
6	Е	Read/Write Enable Signal							
7	DB0								
8	DB1								
9	DB2								
10	DB3	Dioplay Data Signal							
11	DB4	Display Data Signal							
12	DB5								
13	DB6								
14	DB7								
15	LED – (K)	Please also refer to 6. PCB drawing and description.							
16	LED + (A)	Please also refer to 6. PCB drawing and description.							

### 6. PCB DRAWING AND DESCRIPTION



Note: The PCB drawing just for reference!

### **DESCRIPTION:**

### 6-1-1. The polarity of the pin 15 and the pin 16

12 15	12.14	LED Polarity				
J3,J5	J2,J4	15 Pin	16 Pin			
Each Closed	Each open	Cathode	Anode			
Each open	Each Closed	Anode	Cathode			

Note: on application module, J2=J4= open, J3=J5=0 Ohm

### 6-1-2. The J1 is metal-bezel GND to module GND and J7 is mounting holes GND to module GND.

Note: on application module, J1=J7=0 Ohm

### 6-1-3. The LED resistor should be bridged when the J6 is solder-Bridge.

Note: on application module, J6=open

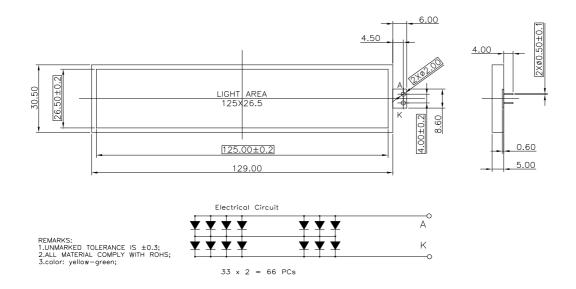
#### 6-1-4. The R7 and R8 is the LED resistor.

Note: on application module R7=R8=5.6 Ohm

### 7. BACKLIGHT ELECTRICAL/OPTICAL SPECIFICATIONS ( $Ta=-20 \sim +70 ^{\circ}C$ )

### Electrcal/Optical Specifications

	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
Forward Voltage	V f	3.7	4.1	4.5	V	If= 270 mA
Forward Current	l f		270		mΑ	
Power Disdipation	Pd		1.11		W	If= 270 mA
Reverse Voltage	VR		10.0		V	
Reverse Current	I R		0.20		mΑ	
Luminous Intensity	ΙV	100	150		cd/m <sup>2</sup>	If= 270 mA
Emission Wavelength	λp	569	572	575	nm	If = 10mA Ta = 25°C
Spectral Range	$\Delta\lambda$		35		nm	Each chip



### 8. DISPLAY DATA RAM (DDRAM)

																							—Displo			
FIRST	LINE	00	01	02	03	04	05	06	07	08	09	0А	0В	0C	OD	0E	OF	10	11	12	13	-	—DDRAN	l Add	lres:	5
SECOND	LINE	40	41	42	43	44	45	46	47	48	49	4 A	4 B	4C	4 D	4E	4F	50	51	52	53					

### 9. MAXIMUM ABSOLUTE LIMIT

Item	Symbol	Standard value	Unit
Power supply voltage(1)	$V_{DD}$	-0.3~+6.0	V
Power supply voltage(2)	VLCD	V <sub>DD</sub> -10.0~V <sub>DD</sub> +0.3	V
Input voltage	Vin	-0.3~V <sub>DD</sub> +0.3	V
Operating temperature	Topr	-20~+70	$^{\circ}$
Storage temperature	T <sub>stg</sub>	-30~+80	$^{\circ}$

<sup>\*</sup>Voltage greater than above may damage to the Circuit. VDD>V1>V2>V3>V4>V5

### 10. ELECTRICAL CHARACTERISTICS

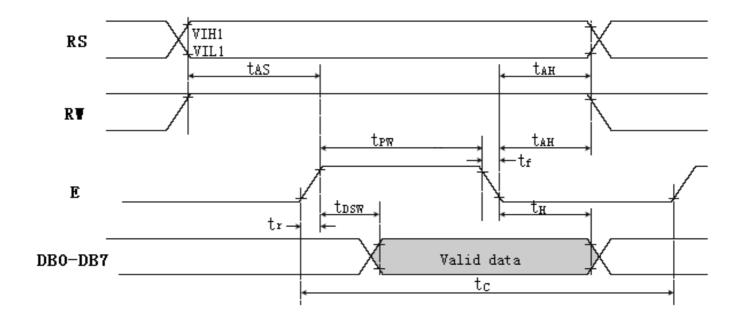
### 10-1 DC Characteristics (V<sub>DD</sub>=4.5V~5.5V,Ta=-20~+70°C)

Itom	Cumbal	Sta	ndard Va	lue	Test	Unit	
Item	Symbol	MIN	TYP	MAX	Condition	Oillt	
Operating Voltage	$ m V_{DD}$	4.5	5	5.5		V	
Supply Current	Idd		0.35	0.6	$V_{DD}=5V, fosc=270kHz$	mA	
LCD Driving Voltage	VLCD	3.0	4.5	7.0	V <sub>DD</sub> -V <sub>5</sub>	V	

### 10-2 AC Characteristics (V<sub>DD</sub>=5V, Ta=-20~+70°C)

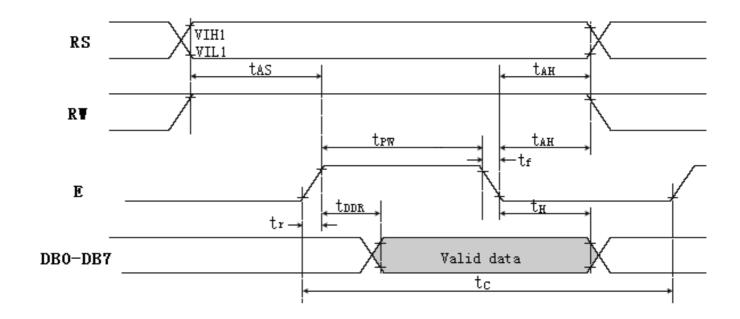
### 10-2-1 Write Mode (Writing Data from MPU to ST7066)

Characteristic	Symbol	Min	Type	Max	Unit	Test PIN
E Cycle Time	tc	1200			ns	E
E Rise Time	t <sub>R</sub>			25	ns	E
E Fall Time	t <sub>F</sub>			25	ns	E
E Pulse width (High,Low)	t <sub>pw</sub>	140			ns	E
R/W and RS Set-up Time	Tas	0			ns	R/W,RS
R/W and RS Hold Time	Тан	10			ns	R/W,RS
Data Set-up Time	T <sub>DSW</sub>	40			ns	DB0~DB7
Data Hold Time	tн	10			ns	DB0~DB7



10-2-2 Read Mode (Reading Data from MPU to ST7066)

Characteristic	Symbol	Min	Type	Max	Unit	Test PIN
E Cycle Time	tc	1200			ns	E
E Rise Time	t <sub>R</sub>			25	ns	E
E Fall Time	t⊧			25	ns	E
E Pulse width (High, Low)	T <sub>PW</sub>	140			ns	E
R/W and RS Set-up Time	Tas	0			ns	R/W,RS
R/W and RS Hold Time	Тан	10			ns	R/W,RS
Data Output Delay Time	todr			100	ns	DB0~DB7
Data Hold Time	t <sub>H</sub>	10			ns	DB0~DB7



### **Product Specification**

### 11. CONTROL AND DISPLAY COMMAND

Command	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Execution time (fosc=270KHz)	Remark
Clear Display	0	0	0	0	0	0	0	0	0	1	1.52ms	Write"20H" to DDRAM. And set DDRAM address to "00H" from AC
Return home	0	0	0	0	0	0	0	0	1	х	1.52ms	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.
Entry mode Set	0	0	0	0	0	0	0	1	I/D	S	37us	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.
Display on/off control	0	0	0	0	0	0	1	D	С	В	37us	D=1: entire display on C=1: cursor on B=1: cursor position on
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	х	х	37us	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.
function Set	0	0	0	0	1	DL	N	F	Х	х	37us	DL: interface data is 8/4 bits N: number of line is 2/1 F: font size is 5x11/5x8
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	37us	Set CGRAM address in address counter
Set DDRAM address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	37us	Set DDRAM address in address counter
Read busy flag& address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Ous	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.
Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	37us	Write data into internal RAM (DDRAM/CGRAM)
Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	37us	Read data from internal RAM (DDRAM / CGRAM)

#### Note:

Be sure the ST7066U is not in the busy state (BF=00 before sending an instruction from the MPU to the ST7066U. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to instruction table for the list of each instruction execution time.

### 12. CHARACTER PATTERN

NO.7066-0T

57-54 53-50	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															
0001	(2)															
0010	(3)															
0011	(4)															
0100	(5)															
0101	(6)															
0110	(7)															
0111	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(8)															
1110	(7)															
1111	(8)															

### 13. QUALITY DESCRIPTION

### **DEFECT SPECIFICATION:**

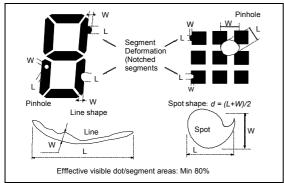
Specific type-related items are covered in this sheet.

a: Table for Cosmetic defects

(Note: nc = not counted).

Sizes and number of defects

(Max. Qty)



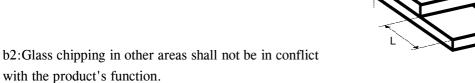
Examples/

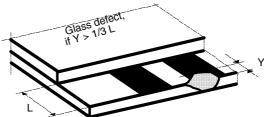
Shapes

b: Glass defects

b1:Glass defects at contact ledge

Defect Type	Max. defect size [µm] d or L	Max. Quantity.
Black or White Spots	d ≤ 150	nc
	$150 < d \le 300$	5
Black or White Lines	 W ≤ 10	nc
	L ≤ 5000 W ≤ 30	3
	L ≤ 2000 W ≤ 50	2
Pinhole	$\begin{array}{c} d \le 150 \\ 150 < d \le 300 \end{array}$	nc 1/segme nt
(Total	(5)	
Segment Deformation	W ≤ 100	nc
Bubble (e.g. under pola)	d ≤ 150	nc
	200 < d ≤ 400	2





### 14. MODULE ACCEPT QUALITY LEVEL (AQL)

14.1 AQL Standard Value: Fatal Defect =0.1, Major Defect=0.65; Minor Defect =2.5.

14.2 Curtailed Inspection Scheme

Type	Batch Qty	inspection Qty	AQL value	pass	Reject
	350PCS<	125pcs	0.1	0	1
module product	1000PCS		0.65	2	3
			2.5	7	8
	200PCS<	80pcs	0.1	0	1
	350PCS		0.65	1	2
			2.5	5	6
	<200PCS	32pcs	0.1	0	1
			0.65	0	1
			2.5	4	5
Module	<200PCS	All	/	/	The sample will be reject
sample		inspected			when the fateful
	>200PCS	125pcs			defect > 2pcs or main
		-			defect > 5pcs.

Notes: 1). Batch QTY is the production amount that Production department ship to QA department.

2). All of product will be inspected if the batch QTY less than inspected QTY.

3). Each batch fixed to be 500pcs.

### 15. RELIABILITY TEST

Operating lifetime: Longer than 50000 hours (at room temperature without direct irradiation of sunlight) Reliability characteristics shall meet following requirements.

TEMPERATURE TESTS	NORMAL GRADE				
High temperature storage	+80°C x 96hrs				
	(Without Polarizer)				
Low temperature storage	-30°C x 96hrs				
High temperature operation	+70°C x 96hrs				
Low temperature operation	-20°C x 96hrs				
High temperature, High humidity	+70°C x 95%RH x 96hrs				
	(Without Polarizer)				
Thermal shock	-20°C x 30min. ◆ 10s				
Vibration test	Frequency x Swing x Time 40Hz x 4mm x 4hrs				
Drop test	Drop height x Times 1.0m x 6times				

### 16. LCD MODULES HANDLING PRECAUTIONS

- Please remove the protection foil of polarizer before using.
- The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance come into contact with your skin or clothes promptly wash it off using soap and water.
- Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarize carefully.
- To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
  - -Be sure to ground the body when handling the LCD module.
  - -Tools required for assembly, such as soldering irons, must be properly grounded.
  - -To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
  - -The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

#### Storage precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0°C). Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

#### 17. OTHERS

- Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- If the LCD modules have been operating for a long time showing the same display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
  - Exposed area of the printed circuit board
  - Terminal electrode sections