

27.07.2022

GENERAL SPECIFICATION

MODULE NO. :

DEM 20485 SYH-LY-CYR22

CUSTOMER P/N

VERSION NO.	CHANGE DESCRIPTION	DATE
0	ORIGINAL VERSION	31.03.2004
1	CHANGE IC	14.08.2008
2	Correct the PCB drawing on Page 4	27.06.2022

PREPARED BY: CXG

DATE: 27.06.2022

DATE: 27.06.2022

APPROVED BY: MH

CONTENTS

1. FUNCTIONS & FEATURES	- 2
2. MECHANICAL SPECIFICATIONS	- 2
3. BLOCK DIAGRAM	- 3
4. EXTERNAL DIMENSIONS	- 3
5. PIN ASSIGNMENT	- 4
6. PCB DRAWING AND DESCRIPTION	- 4
7. BACKLIGHT VOLTAGE & CURRENT	- 5
8. DISPLAY DATA RAM	- 6
9. MAXIMUM ABSOLUTE POWER RATINGS	- 6
10. ELECTRICAL CHARACTERISTICS	- 7
11. CONTROL AND DISPLAY COMMAND	- 9
12. STANDARD CHARACTER PATTERN	- 10
13. LCD MODULES HANDLING PRECAUTIONS	- 11
14. OTHERS	- 11

1. FUNCTIONS & FEATURES

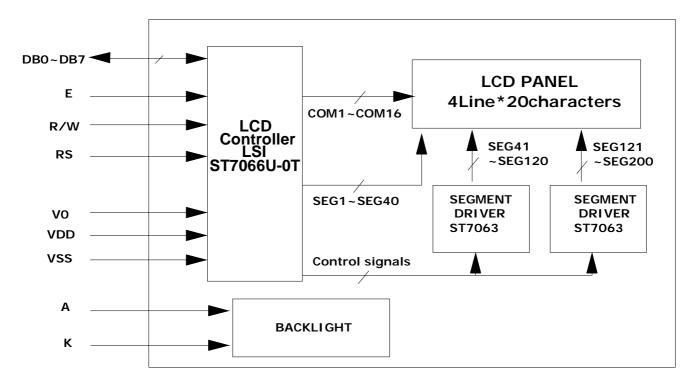
МО	DULE NAME	LCD TYPE
DEM 204	85 SYH-LY-CYR22	STN Yellow Green Transflective Positive Mode
I	Viewing Direction	: 6 O'clock
I	Driving Scheme	: 1/16 Duty Cycle, 1/5 Bias
I	Power Supply Voltag	ge : 5.0V (typ.)
I	Backlight Color	: Yellow Green
I	VLCD	: 4.5V (typ.)
I.	Display contents	: 20 x 4 Characters
I.	Internal Memory	: CGROM (10080 bits)
		: CGRAM (64 x 8 bits)
		: DDRAM (80 x 8 bits for Digits)
I	CGROM	: CGROM of the ST7066U-0T
I	Operating Temperatu	$: -20^{\circ}C \text{ to } +70^{\circ}C$
I	Storage Temperature	: -30°C to +80°C
I	Interface	: Easy Interface with a 4-bit or 8-b

2. MECHANICAL SPECIFICATIONS

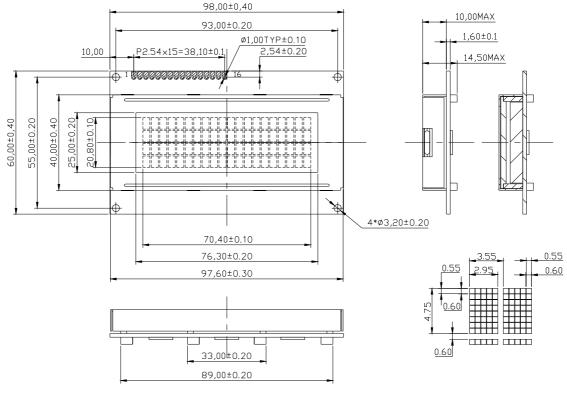
- I Module Size
- I Character Pitch
- I Character Size
- Character Font
- I Dot Size
- I Dot Pitch
- I Dot Gap

- : 98.00 x 60.00 x 14.50 mm (max.)
- : 3.55 x 5.35 mm
- : 2.95 x 4.75 mm
- : 5 x 8 dots
- : 0.55 x 0.55 mm
- : 0.60 x 0.60 mm
- : 0.05 mm

3. BLOCK DIAGRAM



4. EXTERNAL DIMENSIONS

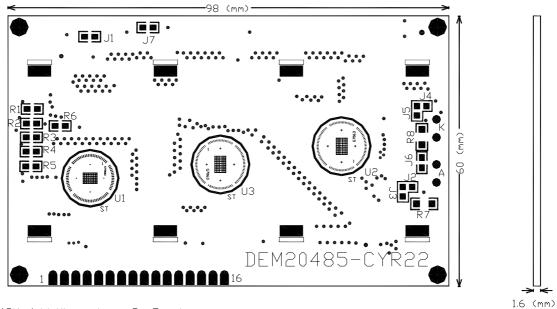


NDTES: UNTELERANCE IS ±0.3mm.

5. PIN ASSIGNMENT

Pin No.	Symbol	Function
1	VSS	Ground terminal of module.
2	VDD	Power terminal of module+5.0V.
3	V0	Power Supply for liquid crystal drive.
4	RS	Register select RS = 0Instruction register RS = 1Data register
5	R/W	Read /Write R/W = 1Read R/W = 0Write
6	E	Enable
7	DB0	
8	DB1	
9	DB2	Bi-directional data bus, data transfer is performed once, thru DB0 to
10	DB3	DB7, in the case of interface data. Length is 8-bits; and twice, thru
11	DB4	DB4 to DB7 in the case of interface data length is 4-bits. Upper four
12	DB5	bits first then lower four bits.
13	DB6	
14	DB7	
15	LED – (K)	Please also refer to 6.1 PCB drawing and description.
16	LED + (A)	Please also refer to 6.1 PCB drawing and description.

6. PCB DRAWING AND DESCRIPTION



REMARK: Add UL mark on Top⊡verlay

Note: The part no. DEM20485CYR22 is printed on the PCB.

DESCRIPTION:

6-1-1. The polarity of the pin 15 and the pin 16 :

	symbol	12 15	12 14	LED Polarity			
symbol	state	J3,J5	J2,J4	15 Pin	16 Pin		
J2,J4	Each solder-bridge	Each open	Each closed	Anode	Cathode		
J3,J5	Each solder-bridge	Each closed	Each open	Cathode	Anode		

Note: In application module, J3=J5=closed, J2=J4=open

6-1-2. The metal-bezel is set on ground when the J1 is closed.

Note: In application module, J1=closed

6-1-3. The LED resistor can be bridged when the J6 is closed

Note: In application module, J6=open

6-1-4.The R7 and the R8 are the LED resistor.

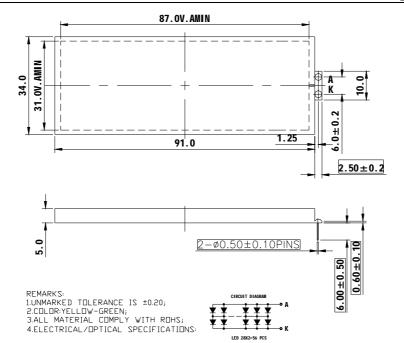
Note: R7=R8=10ohm

6-1-5. The mounting holes is set on ground when J7 is closed.

Note: In application module J7=closed

7. BACKLIGHT & SWITCH (Ta=-20~+70°C)

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
Forward Voltage	Vf		4.1	4.5	V	lf=280mA
Forward Current	lf		280	560	mA	
Power Dissipation	Pd		1.15		W	lf=280mA
Reverse Voltage	Vr			10.0	V	
Reverse Current	Ir			0.20	mA	
Luminous Intensity	Iv	100	150		cd/m ²	lf=280mA
Luminous Uniformity	¦¤2v	75			%	lf=280mA
Full and an Warral an add	L IR	5/0	F 70			lf=10mA Ta=25° C
Emission Wavelength	l Ë	569	572	575	nm	Each chip



8. DISPLAY DATA RAM (DDRAM)

DISPLAY POSITION -----

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	◀	
FIRST LINE	00	01	02	03	04	05	06	07	08	09	OA	OB	ос	0D	OE	OF	10	11	12	13	◀	RAM DRESS
SECOND LINE	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	50	51	52	53		
THIRDD LINE	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F	20	21	22	23	24	25	26	27		
Fourth Line	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F	60	61	62	63	64	65	66	67		

9. MAXIMUM ABSOLUTE POWER RATINGS (Ta=25°C)

Item	Symbol	Standard value	Unit
Power supply voltage (1)	Vdd	-0.3~+7.0	V
Power supply voltage (2)	V ₀	V _{DD} -15.0~V _{DD} +0.3	V
Input voltage	Vin	-0.3~V _{DD} +0.3	V
Operating temperature	Topr	-20~+70	°C
Storage temperature	Tstg	-30~+80	°C

*Voltage greater than above may damage to the Circuit.

VDD>V1>V2>V3>V4>V5

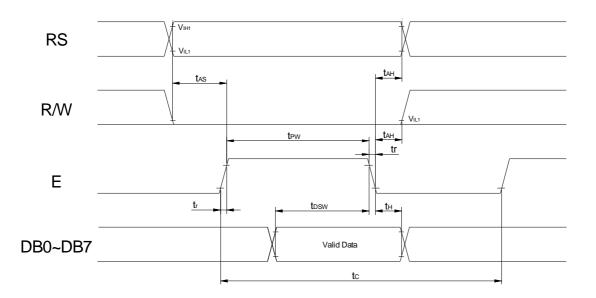
10. ELECTRICAL CHARACTERISTICS

10-1 DC Characteristics(VDD=4.5V~5.5V,Ta=-20~+70°C)

Item	Symbol	Star	dard Val	lue	Test	Unit	
nem	Symbol	MIN	TYP	MAX	Condition	Unit	
Operating Voltage	V _{DD}	4.5	5	5.5		V	
Supply Current	I _{DD}		0.35	0.6	V _{DD} =5V,fosc=270kHz	mA	
LCD Driving Voltage	VLCD	3.0	4.5	13.0	VDD-V5 (1/5,1/4 Bias)	V	

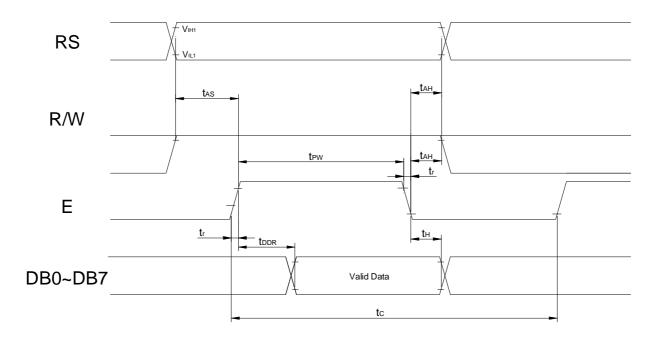
10-2 AC Characteristics (V_{DD} =4.5V~5.5V, Ta=-20~+70°C) 10-2-1. Write mode (writing data from MPU to module)

Characteristic	Symbol	Min	Тур	Max	Unit	Test PIN
E Cycle Time	tc	1200			ns	E
E Rise Time	t _R			25	ns	E
E Fall Time	tF			25	ns	E
E Pulse width (High, Low)	tw	140			ns	E
R/W and RS Set-up Time	ts∪1	0			ns	R/W, RS
R/W and RS Hold Time	t _{H1}	10			ns	R/W, RS
Data Set-up Time	ts∪2	40			ns	DB0~DB7
Data Hold Time	t _{H2}	10			ns	DB0~DB7



10-2-2.Read mode (Reading data from module to MPU)

Characteristic	Symbol	Min	Тур	Max	Unit	Test PIN
E Cycle Time	tc	1200			ns	E
E Rise Time	t _R			25	ns	E
E Fall Time	t⊧			25	ns	E
E Pulse width (High, Low)	tw	140			ns	E
R/W and RS Set-up Time	ts∪	0			ns	R/W, RS
R/W and RS Hold Time	tн	10			ns	R/W, RS
Data output Delay Time	t⊳			100	ns	DB0~DB7
Data Hold Time	tон	5			ns	DB0~DB7



11. CONTROL AND DISPLAY COMMAND

Command	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Execution time (fosc=270KHz)	Remark
Clear Display	0	0	0	0	0	0	0	0	0	1	1.52ms	Write"20H" to DDRAM. And set DDRAM address to "00H" from AC
Return home	0	0	0	0	0	0	0	0	1	X	1.52ms	Set DDRAM address to "00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.
Entry mode Set	0	0	0	0	0	0	0	1	I/D	S	37us	Sets cursor move direction and specifies display shift. These operations are performed during data write and read.
Display on/off control	0	0	0	0	0	0	1	D	С	В	37us	D=1: entire display on C=1: cursor on B=1: cursor position on
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	x	X	37us	Set cursor moving and display shift control bit, and the direction, without changing DDRAM data.
function Set	0	0	0	0	1	DL	Ν	F	x	X	37us	DL: interface data is 8/4 bits N: number of line is 2/1 F: font size is 5x11/5x8
Set CGRAM address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	37us	Set CGRAM address in address counter
Set DDRAM address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	37us	Set DDRAM address in address counter
Read busy flag& address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Ous	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.
Write data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	37us	Write data into internal RAM (DDRAM/CGRAM)
Read data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	37us	Read data from internal RAM (DDRAM / CGRAM)

Note:

Be sure the ST7066U is not in the busy state (BF=00 before sending an instruction from the MPU to the ST7066U. If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself. Refer to instruction table for the list of each instruction execution time.

12. STANDARD CHARACTER PATTERN (ST7066U-0T)

NØ.7066-0T

67-64 63-60	0000	0001	0010	001 1	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	11 11
0000	CG RAM (1)															
0001	(2)															
0010	(3)															
0011	(4)															
0 100	(5)															
0101	(6)															
0110	7)															
0111	(8)															
1000	(1)															
1001	(2)															
1010	3															
1011	(4)															
1100	(5)															
1101	(8)															
1110	(7)															
1111	(8)															

13. LCD MODULES HANDLING PRECAUTIONS

- **n** Please remove the protection foil of polarizer before using.
- **n** The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- **n** If the display panel is damaged and the liquid crystal substance inside it leaks out, do not get any in your mouth. If the substance comes into contact with your skin or clothes promptly wash it off using soap and water.
- **n** Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
- **n** The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- **n** To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

-Be sure to ground the body when handling the LCD module.

-Tools required for assembly, such as soldering irons, must be properly grounded.

-To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

-The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

n Storage precautions

When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps. Keep the modules in bags designed to prevent static electricity charging under low temperature / normal humidity conditions (avoid high temperature / high humidity and low temperatures below 0° C).Whenever possible, the LCD modules should be stored in the same conditions in which they were shipped from our company.

.14. OTHERS

- **n** Liquid crystals solidify at low temperature (below the storage temperature range) leading to defective orientation of liquid crystal or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subjected to a strong shock at a low temperature.
- **n** If the LCD modules have been operating for a long time showing the same display, patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. Abnormal operating status can be resumed to be normal condition by suspending use for some time. It should be noted that this phenomena does not adversely affect performance reliability.
- **n** To minimize the performance degradation of the LCD modules resulting from caused by static electricity, etc. exercise care to avoid holding the following sections when handling the modules:
 - Exposed area of the printed circuit board
 - Terminal electrode sections